2SB0933 (2SB933)

Silicon PNP epitaxial planar type

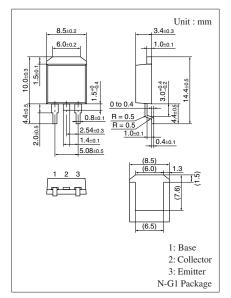
For Power switching Complementary to 2SD1256

Features

- \bullet Low collector-emitter saturation voltage $V_{\mbox{CE(sat)}}$
- \bullet Satisfactory linearity of forward current transfer ratio h_{FE}
- \bullet Large collector current I_{C}
- N type package enabling direct soldering of the radiating fin to the printed circuit board, etc. of small electronic equipment.

Absolute Maximum Hatings $T_{\rm C} = 25$ C						
Parameter		Symbol	Rating	Unit		
Collector-base voltage (Emitter open)		V _{CBO}	-130	V		
Collector-emitter voltage (Base open)		V _{CEO}	-80	V		
Emitter-base voltage (Collector open)		V _{EBO}	-7	V		
Collector current		I _C	-5	А		
Peak collector current		I _{CP}	-10	А		
Collector power		P _C	40	W		
dissipation	$T_a = 25^{\circ}C$		1.3			
Junction temperature		Tj	150	°C		
Storage temperature		T _{stg}	-55 ~ +150	°C		
Peak collector current Collector power dissipation Junction temperature	$T_a = 25^{\circ}C$	I _C I _{CP} P _C T _j	-10 40 1.3 150	A W °C		

Absolute Maximum Ratings $T_C = 25^{\circ}C$



Note) Self-supported type package is also prepared.

Electrical Characteristics $T_C = 25^{\circ}C \pm 3^{\circ}C$

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-emitter voltage (Base open)	V _{CEO}	$I_{\rm C} = -10 \text{ mA}, I_{\rm B} = 0$	-80			V
Collector-base cutoff current (Emitter open)	I _{CBO}	$V_{CB} = -100 \text{ V}, I_E = 0$			-10	μΑ
Emitter-base cutoff current (Collector open)	I _{EBO}	$V_{EB} = -5 V, I_C = 0$			-50	μΑ
Forward current transfer ratio	h _{FE1}	$V_{CE} = -2 V, I_C = -0.1 A$	45			
	h _{FE2} *	$V_{CE} = -2 V, I_C = -2 A$	90		260	
Base-emitter voltage	V _{BE(sat)}	$I_{\rm C} = -4$ A, $I_{\rm B} = -0.2$ A			-1.5	V
Collector-emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = -4$ A, $I_{\rm B} = -0.2$ A			- 0.5	V
Transition frequency	f _T	$V_{CE} = -10 \text{ V}, I_C = -0.5 \text{ A}, f = 10 \text{ MHz}$		30		MHz
Turn-on time	t _{on}	$I_{\rm C} = -2 {\rm A},$		0.13		μs
Storage time	t _{stg}	$I_{B1} = -0.2 \text{ A}, I_{B2} = 0.2 \text{ A}$		0.5		μs
Fall time	t _f	$V_{\rm CC} = -50 \text{ V}$		0.13		μs

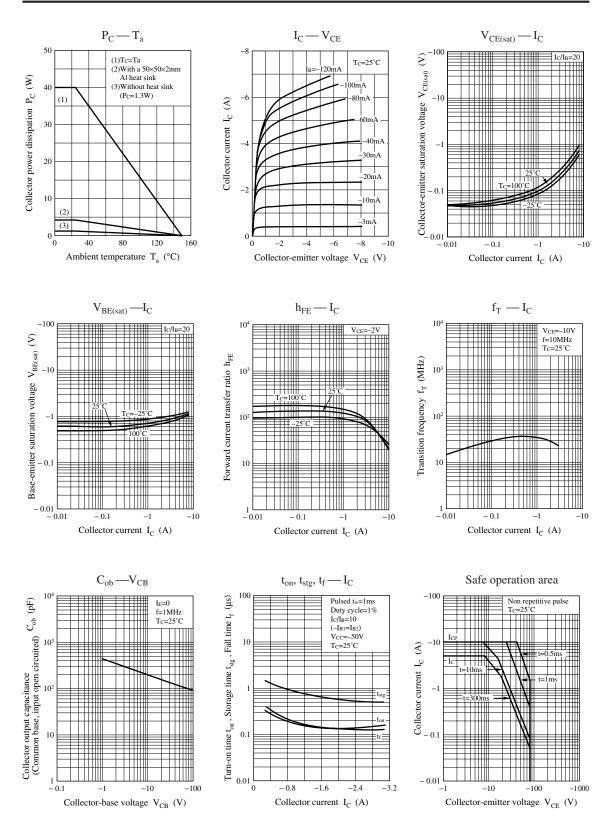
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

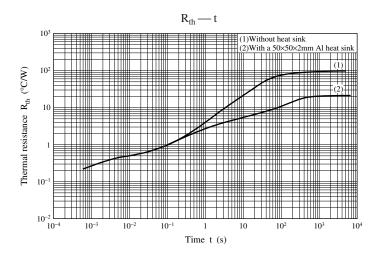
2. *: Rank classification

Rank	Q	Р		
h _{FE2}	90 to 180	130 to 260		

Note) The part number in the parenthesis shows conventional part number.

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